



Algorithms and Data Structures: Efficient and Cache-Oblivious

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ABSTRACT: *The Computer Architecture consists of memory hierarchy which varies from fast and expensive to cheap and slower. And with increasing speed of the processors the time taken to transfer data between memories is more than the actual time taken to process it. To alleviate this cache-oblivious algorithms and data structures are developed. In this paper we discuss various cache-oblivious data structures like B-tree and hash table implementing cache-oblivious hashing; and cache-oblivious algorithms like integer multiplication and string sorting with improvement.*